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### Microelectronics - Circuit Analysis and Design (4th ...

Microelectronics: Circuit Analysis and Design, 4th edition Chapter 1 By D. A. Neamen Exercise Solutions \_\_\_\_\_ EX1.8  $(V) V_{PS} = I_D R + V_D$  and  $I_D \cong I_S \exp \left( \frac{V_D}{V_T} \right) (4 - V_D)$  so  $4 = I_D (4 \times 10^3) + V_D \Rightarrow I_D = 4 \times 10^3$  and  $(V) I_D = (10^{-12}) \exp \left( \frac{V_D}{0.026} \right)$  By trial and error, we find  $I_D \cong 0.866$  mA and  $V_D \cong 0.535$  V. \_\_\_\_\_

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Assume that the BJT in the common-emitter circuit shown in Figure 8.4 has limiting factors of  $\beta$ ,  $r_{ce}$ , and  $r_{be}$ . Neglecting second breakdown effects, determine the minimum value of  $R_L$  such that the Q-point of the transistor always stays within the safe operating area for: (a)  $\beta = 100$ , and (b)  $\beta = 200$ . In each case, determine the maximum collector current and maximum transistor power dissipation.

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